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(71) Applicant (for all designated States except US): **KONIN-
KLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL];
Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **BINK, Adrianus, J.**
[NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven
(NL). **STRAVERS, Paul** [NL/NL]; c/o Prof. Holstlaan 6,
NL-5656 AA Eindhoven (NL).

(74) Agent: **DE JONG, Durk, J.**; Prof. Holstlaan 6, NL-5656
AA Eindhoven (NL).

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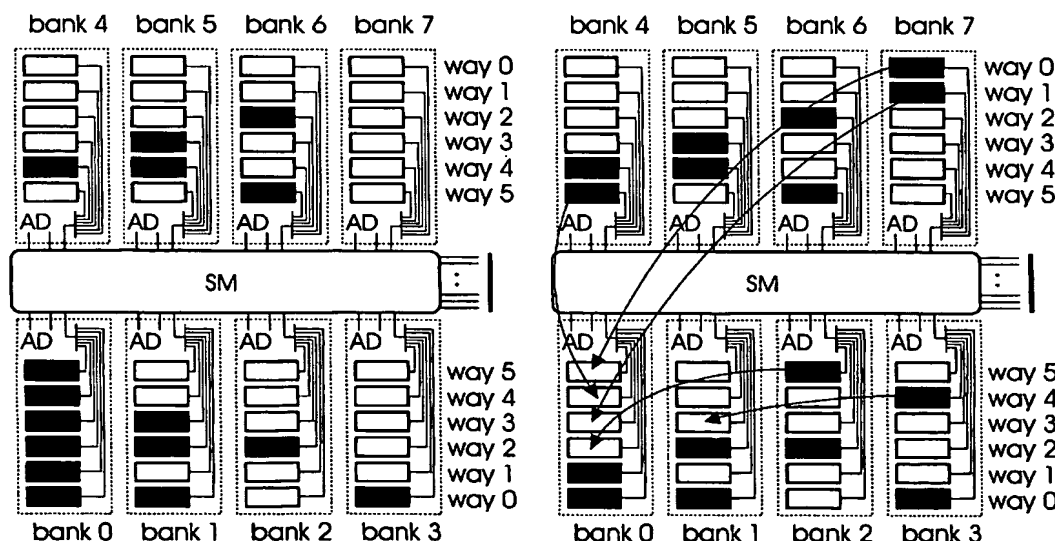
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(54) Title: INTEGRATED CIRCUIT AND A METHOD OF CACHE REMAPPING



(57) Abstract: An integrated circuit is provided with at least one processing unit (TM), a cache memory (L2 BANK) having a plurality of memory modules, and remapping means (RM) for performing an unrestricted remapping within said plurality of memory modules. Accordingly, faulty modules can be remapped without limitations in order to optimise the utilization of the memory modules by providing an even distribution of the faulty modules.



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